

PATENT
Customer No. 22,852
Attorney Docket No. 04329.2444-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Hiroshi TOYODA, et al.) Group Art Unit: *Not Yet Assigned*
)
Div. of Application No.: 09/677,743) Examiner: *Not Yet Assigned*
)
Filed: Herewith)
)
For: METHOD OF MANUFACTURING)
SEMICONDUCTOR DEVICE AND)
SEMICONDUCTOR DEVICE)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicants bring to the attention of the Examiner the documents listed on the attached PTO-1449. Applicants file this Information Disclosure Statement together with the above-referenced divisional application.

Applicants previously submitted copies of the listed documents, or they were cited by the Examiner in prior application No. 09/677,743, filing date October 3, 2000, upon which Applicants rely for the benefits provided in 35 U.S.C. § 120. Applicants respectfully request that the Examiner consider the listed documents, and indicate that they were considered by making appropriate notations on the attached form.

The following are listed on the accompanying PTO-1449 and are in a non-English language. The statement of relevance, as indicated in the parent case, is the following:

1. Japanese Patent Application No. 11-102911 discloses an aluminum layer on copper wiring (Fig. 2(c)). The aluminum layer was deposited directly on copper wiring so that a serious reaction would occur between the two components.

2. Japanese Patent Application No. 6-275612 corresponds to U.S. Patent No. 5,470,789 which is also listed on the attached PTO-1449.

This submission does not represent that a search has been made or that no better art exists, and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

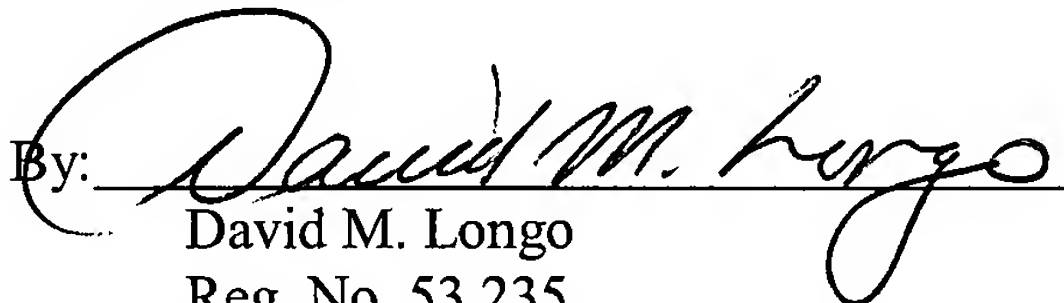
Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

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Dated: June 23, 2003

By: 
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INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2444-01	Div. of Appln. No.	09/677,743
Applicant	Hiroshi TOYODA, et al.		
Filing Date	Herewith	Group:	Not Yet Assigned

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
		5,470,789	11/1995	Misawa			
		5,578,523	11/1996	Fiordalice et al.			
		5,877,076	03/1999	Dai			
		5,880,018	03/1999	Boeck et al.			
		5,985,748	11/1999	Watts et al.			
		6,433,428	08/2002	Watanabe et al.			

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		11-102911	04/1999	Japan			No
		6-275612	09/1994	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	Wolf, "Silicon Processing for the VLSI Era, Vol. 2 - Process Integration," 1990, Lattice Press, Vol. 2, p. 193

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce